

FIG. 1a - Prior Art

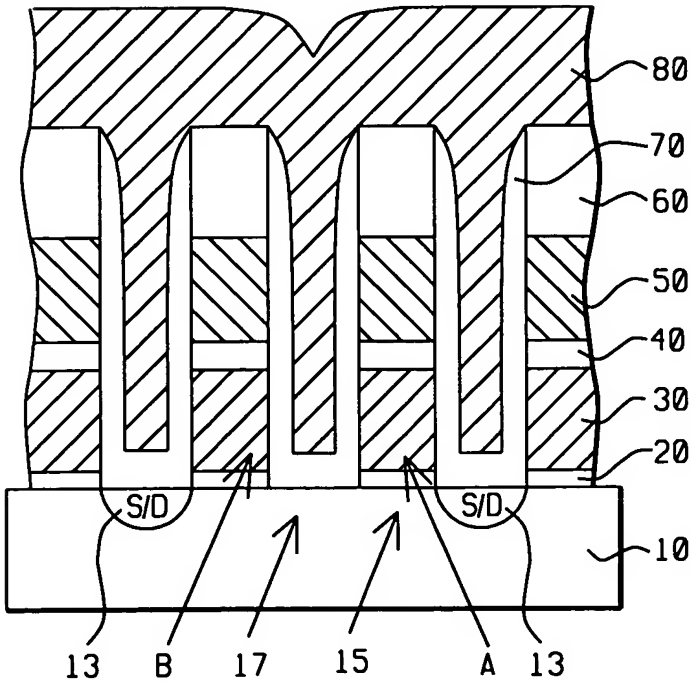


FIG. 1b - Prior Art

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Table of Keys to Figures

D : drain as BIT LINE  
S : source  
CG: control gate  
SG: selected gate as WORD LINE  
TG: transfer gate



: seleceted cell



: un-selected cell

FIG. 1c - Prior Art

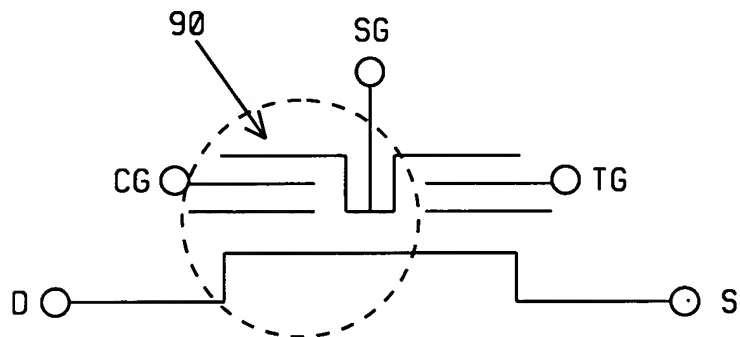


FIG. 1d - Prior Art

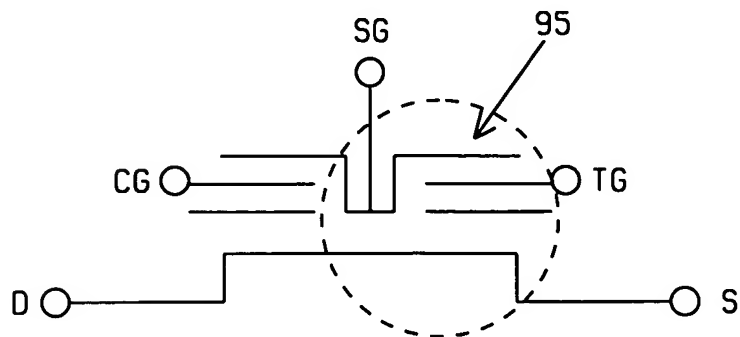


FIG. 1e - Prior Art

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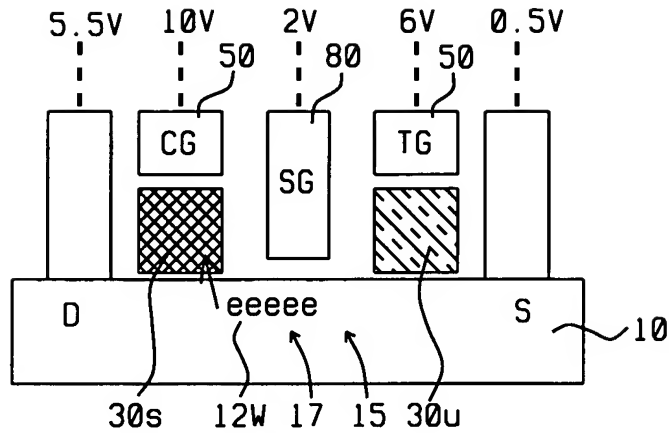


FIG. 2a - Prior Art

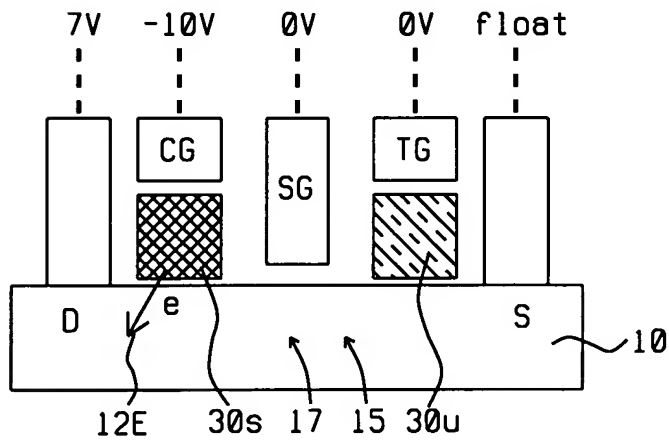


FIG. 2b - Prior Art

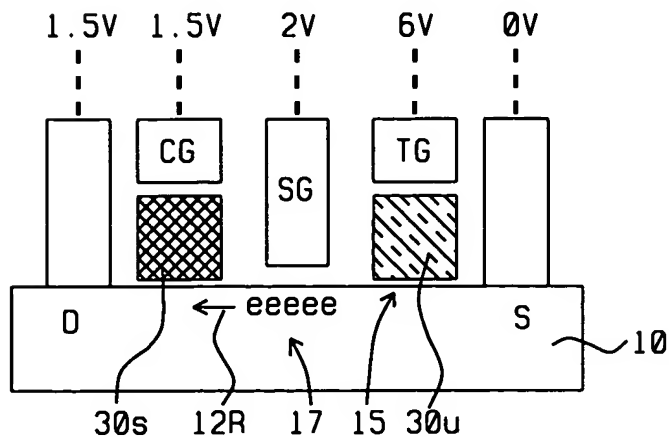


FIG. 2c - Prior Art

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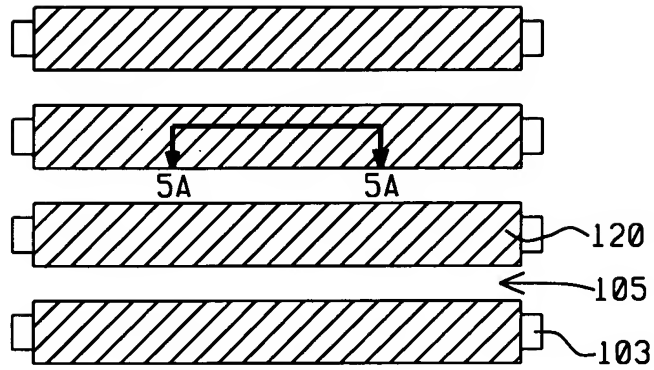


FIG. 4a

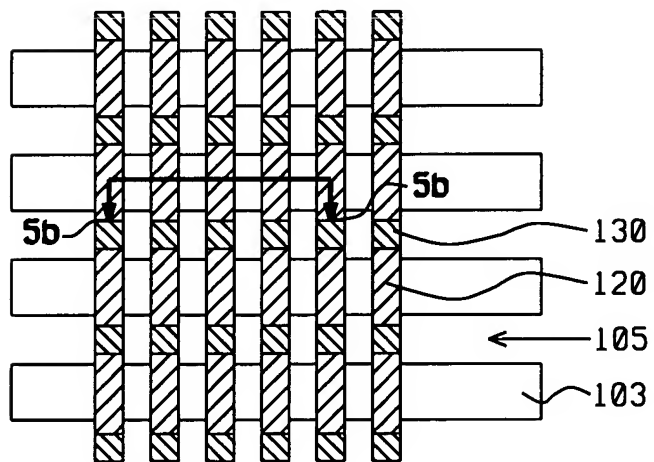


FIG. 4b

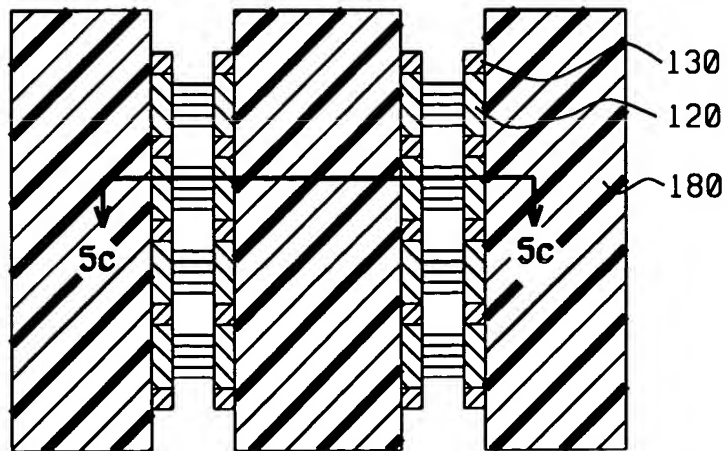


FIG. 4c

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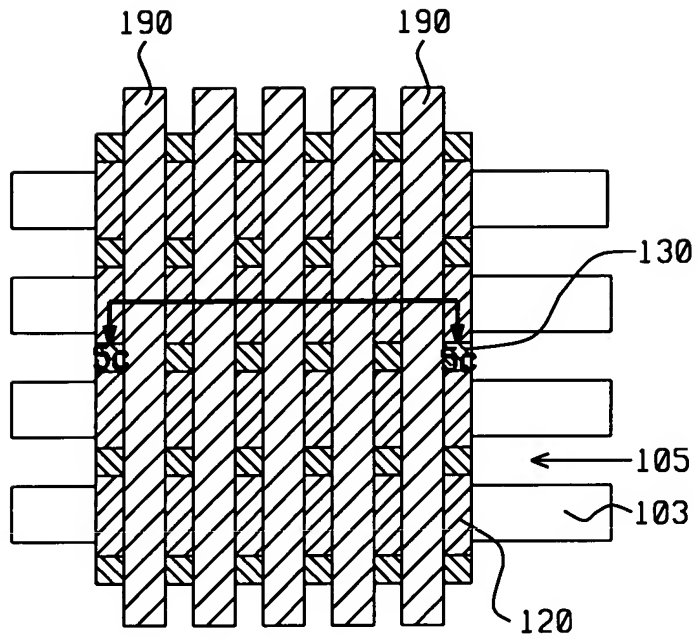


FIG. 4d

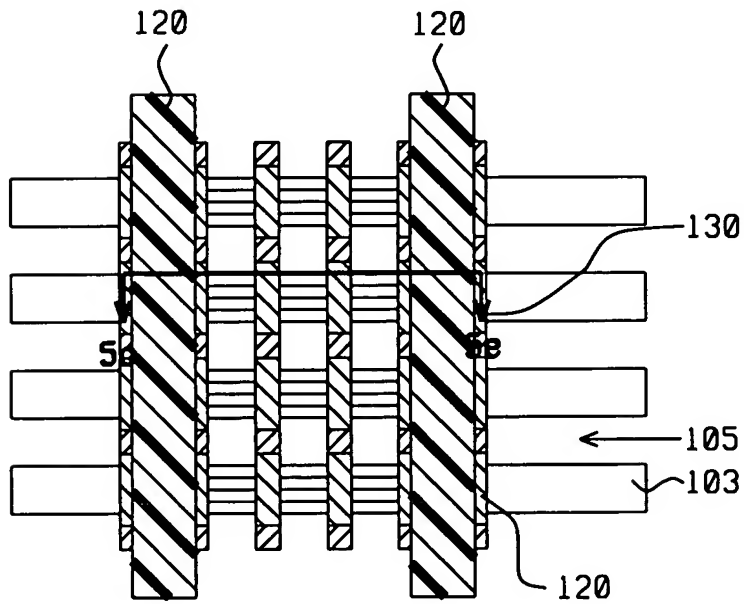


FIG. 4e

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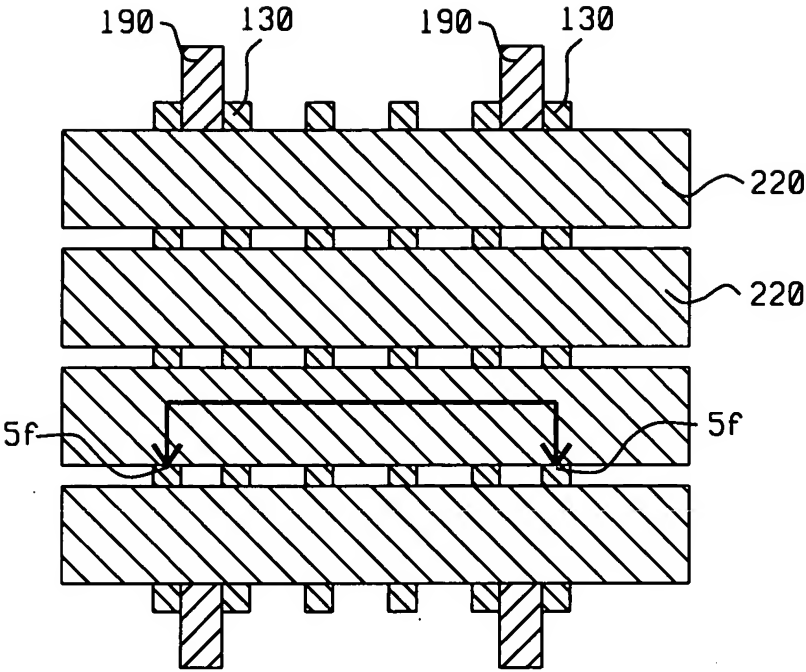


FIG. 4f

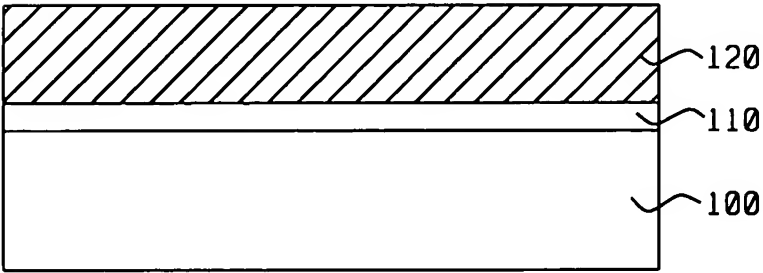


FIG. 5a

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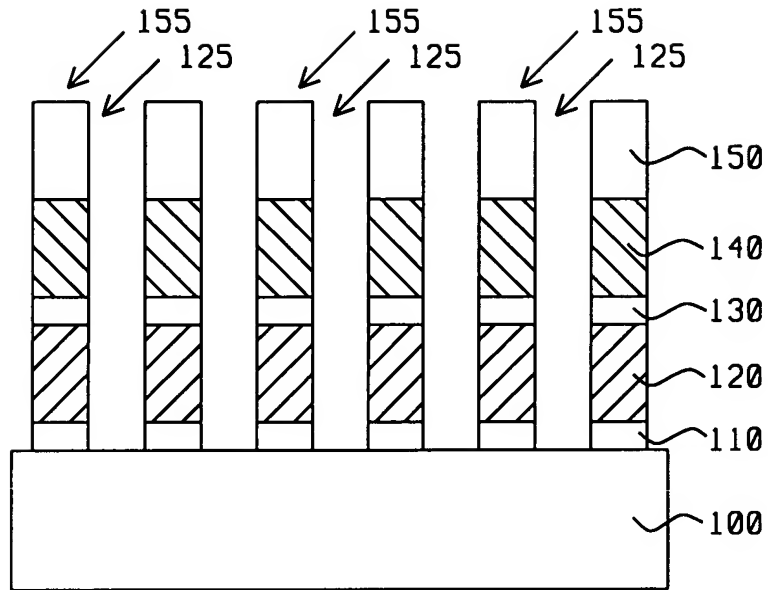


FIG. 5b

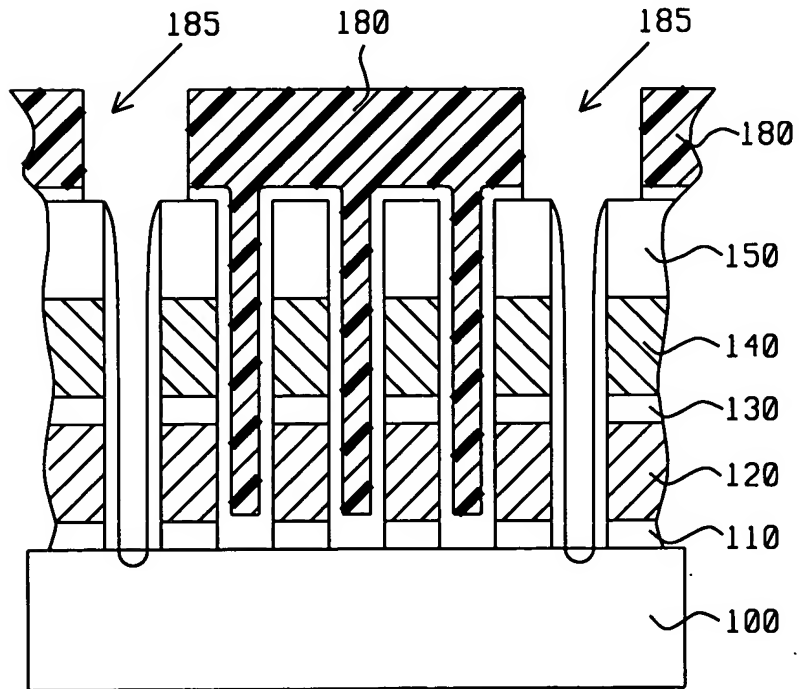


FIG. 5c

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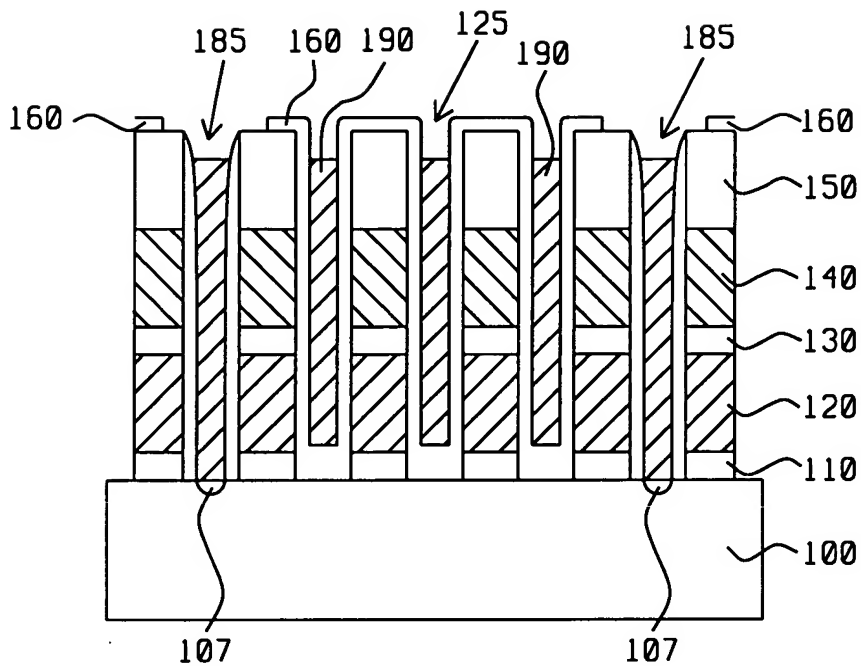


FIG. 5d

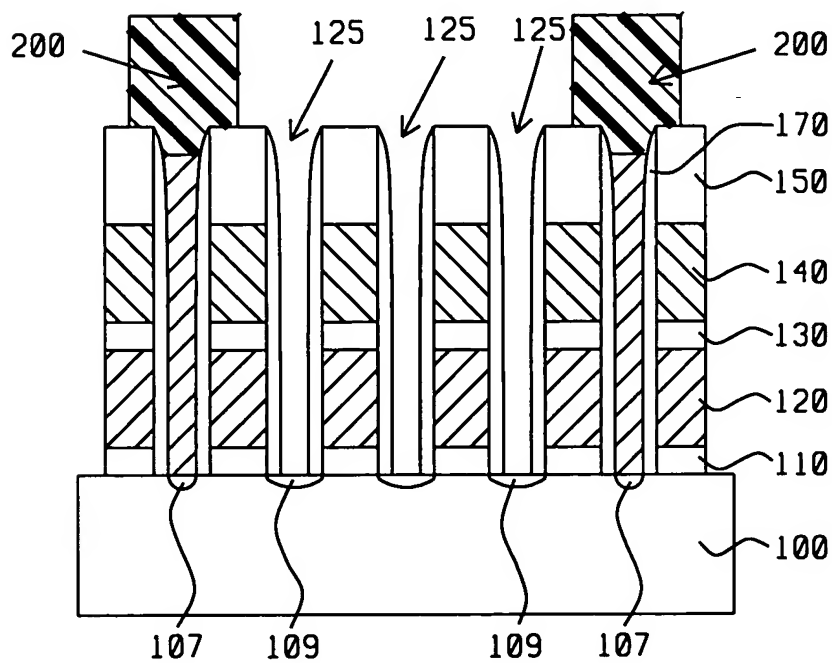


FIG. 5e

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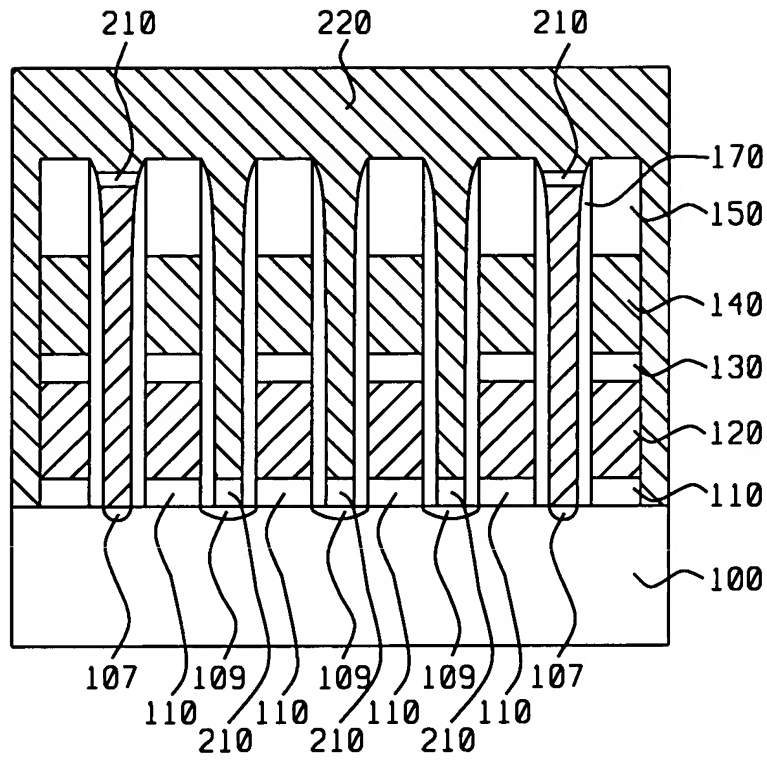


FIG. 5f

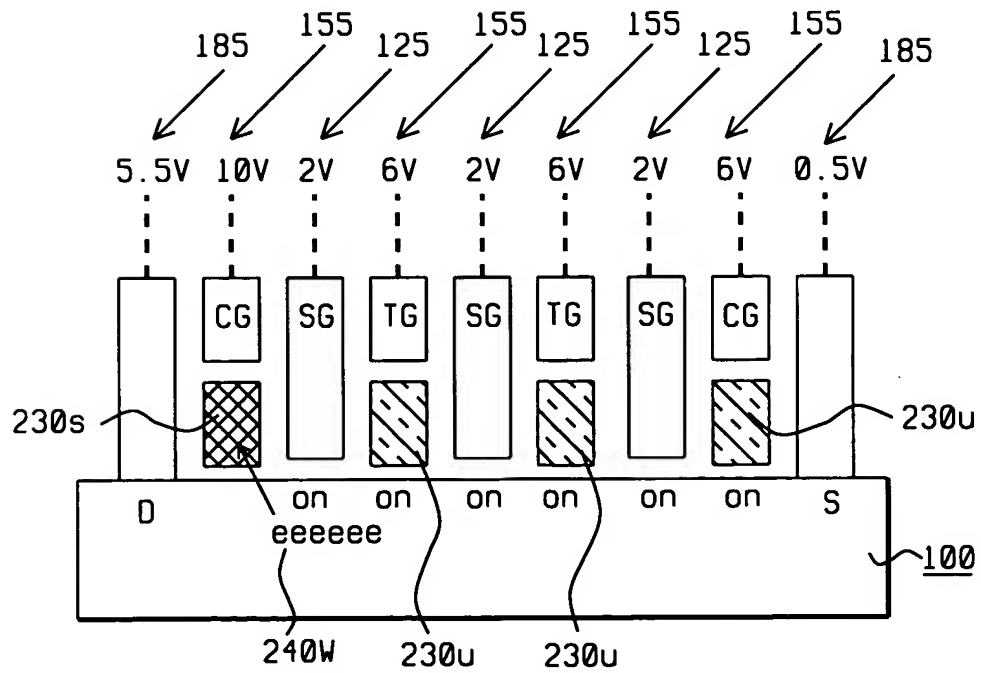


FIG. 6a

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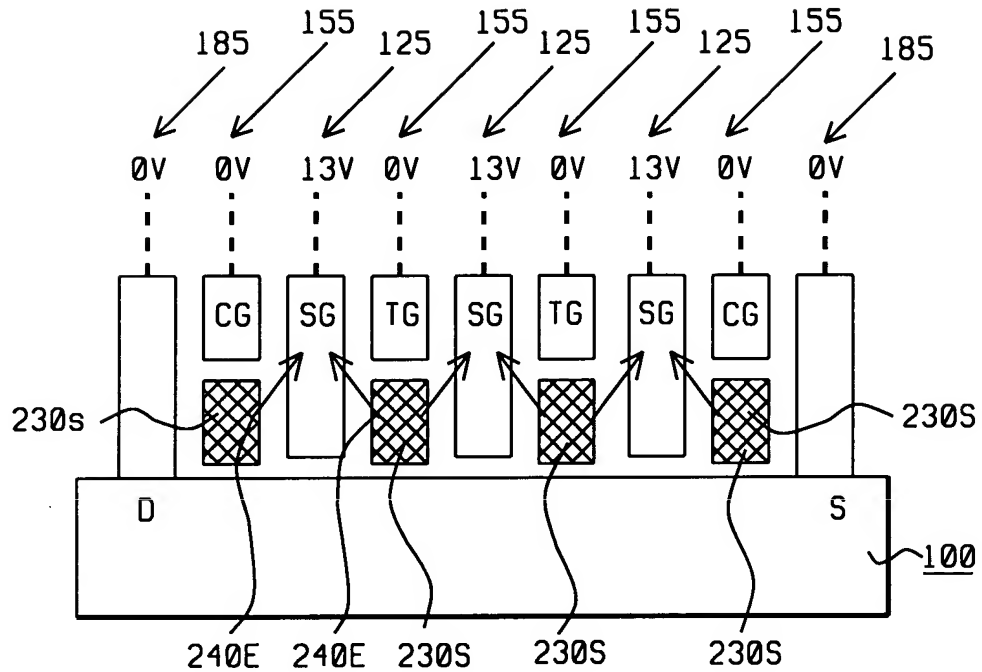


FIG. 6b

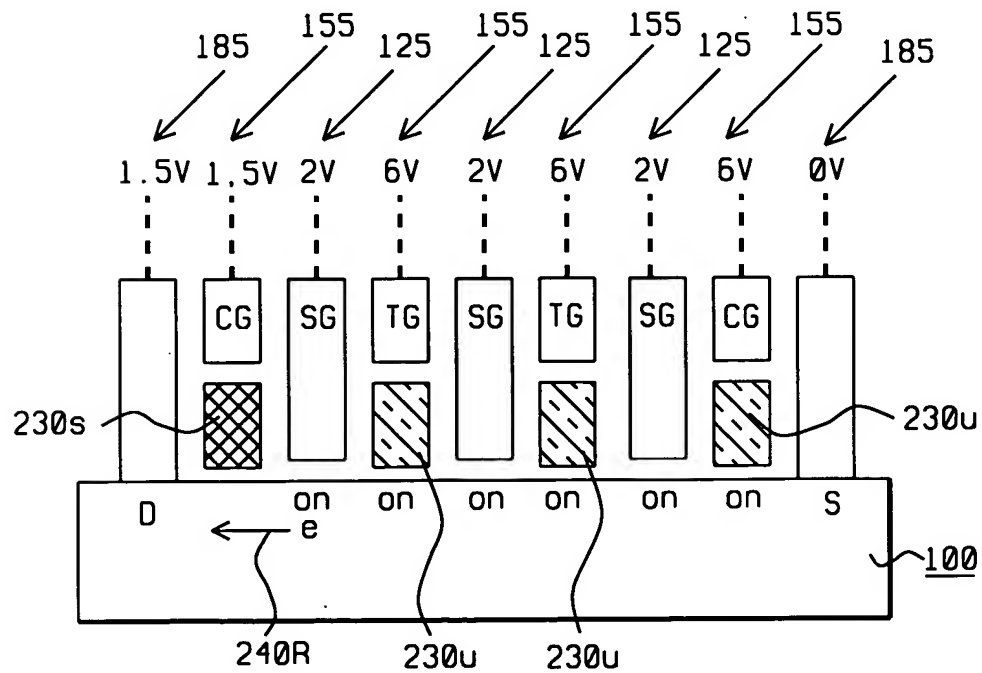
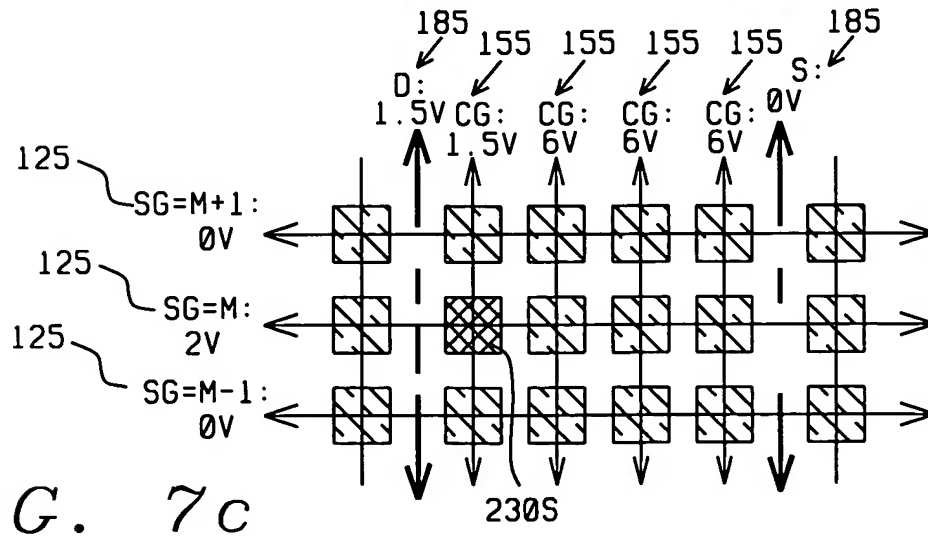
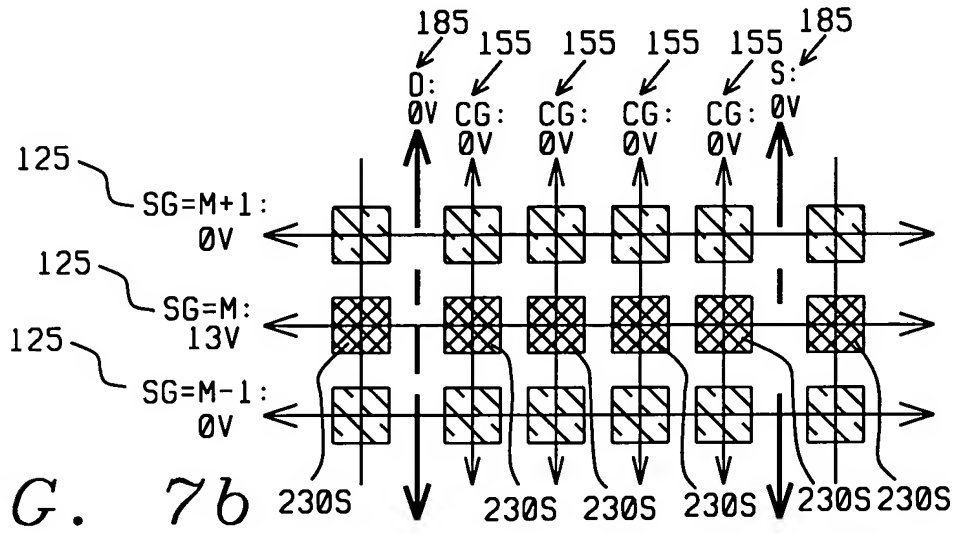
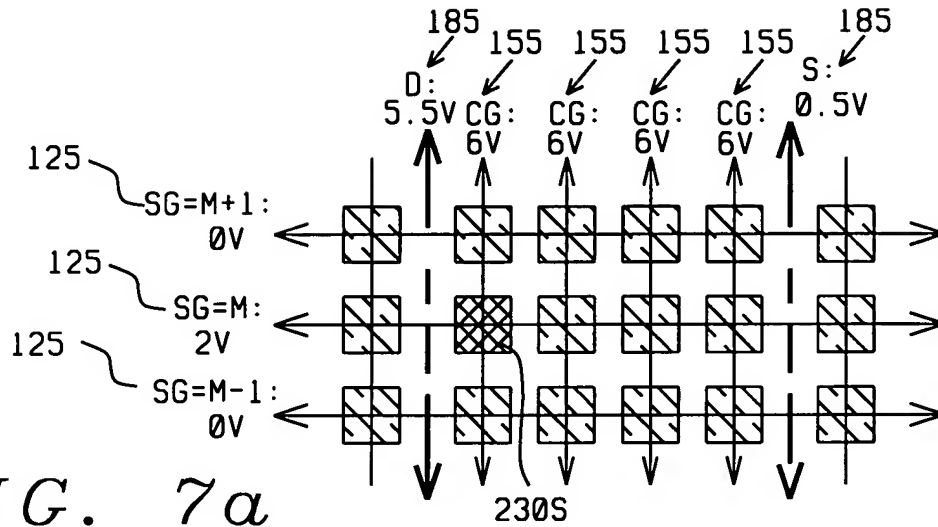


FIG. 6c



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